Double pulse testing

With each internal OJ project focusing on drives for motor control, there is a need for assessing the power module’s characteristics at turn on and turn off. This is critical, since it verifies if the design is complying in terms of stray inductances at the gate and emitter of the transistors in use. OJ would like this double pulse test to be implemented, through software, directly on our hardware, thus reducing the setup time before testing. This project can be further extended with PCB design for automatic testing if desired.

**Your interests**
Programing, fascinated by power electronics (double pulse testing), testing skills.

**Technology stack**
C programing, power electronics, testing design

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